

Notice of References Cited

Application/Control No.

10/006,860

Applicant(s)/Patent Under

Reexamination

MIYAMOTO ET AL.

Examiner

Shane M Thomas

Art Unit

2186

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,101,329 A	08-2000	Graef, Stefan	710/52
	B	US-2001/0043603 A1	11-2001	Yu, Shaohua	370/393
	C	US-6,584,584 B1	06-2003	Smith, Brian L.	714/42
	D	US-6,550,023 B1	04-2003	Brauch et al.	714/42
	E	US-6,614,798 B1	09-2003	Bishop et al.	370/428
	F	US-6,006,286 A	12-1999	Baker et al.	710/22
	G	US-6,609,167 B1	08-2003	Bastiani et al.	710/104
	H	US-5,884,099 A	03-1999	Klingelhofer, Marc	710/52
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Pham & Schmitt, "A High Throughput, Asynchronous, Dual Port FIFO Memory Implemented in ASIC Technology," IEEE, May 1989
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.